PCN Number:	2015012300	01			P	CN Date:	01/28/2015
Title: Datasheet up							
Change Type:	- Communicação		20pt .	- Quan	٠, ٠	30. 11005	
Assembly Site			Design	I	\neg	Wafer Bum	n Sita
Assembly Process	L	M	Data Sheet		=	Wafer Bum	
Assembly Materials							
Mechanical Specification Test Site Wafer Fab Site							
Packing/Shipping/Labeling Test Process Wafer Fab Materi							
□ Wafer Fab Process							
			PCN Details				
Description of Chang	e:						
Texas Instruments Inco		nno	ouncing an information	n only	no	tification.	
The product datasheet(The following change hi				ow.			
bq24190, bq24192i Texas Instruments							
SLUSAW5B – JANUARY 2012–REVISE	ED DECEMBER 2014						www.ti.com
Changes from Revision A (O	ctober 2012) to Re	Revi	sion B				Page
Aligned package description	n throughout datas	shee	ot .				1
	 Aligned package description throughout datasheet						
			ntation Support, Mechanical, F				
 Added Feature: Compatible 	e with MaxLife Tech	chno	logy for Faster Charging Whe	n Used ii	n Co	njunction With bo	27531 1
 Changed V_{SLEEPZ}, V_{BAT_DPL} 	HY, VBATGD, ICHG_20	Opct,	V _{SHORT} , I _{ADPT_DPM} , K _{ILIM} , V _{BTST_}	REFRESH İ	n Ele	ectrical Character	ristics 6
 Added REG00[6:3] = 0110 	(4.36 V) or 1011 (4	(4.76	V) to V _{INDPM_REG_ACC} Test Co	nditions.			8
	Added Typical input current of 1.5 A based on KLIM to I _{ADPT_DPM} Test Conditions						
 Added a MIN value of 435 	Added a MIN value of 435 to K _{ILIM}					8	
Deleted T _{Junction_REG} MIN and MAX							
Added rising to V _{HTF} parameter8							
Deleted V _{REGN} , V _{VBUS} = 5 V, I _{REGN} = 20 mA MAX value					9		
Changed Functional Block Diagram							
Changed Charging Current in Table 4							
Changed REG09[5:4] to REG08[5:4] in Charging Termination section							
Added or when FORCE_20PCT (REG02[0]) bit is set, to Charging Safety Timer description							
Added last paragraph to Charging Safety Timer description							
Added twice to Host Mode and Default Mode description							
Changed REG05[5:4]=11 to REG05[5:4]=00 in Host Mode and Default Mode description							
Changed Charge Current Control Register REG02 Bit 0 description							
Changed Charge Current Control Register REG02 Bit 0 note							
Changed REG05 Bit 0 from JEITA ISET (0°C-10°C) to Reserved							
Changed REG07 Bit 4 from JEITA_VSET (45°C to 60°C) to Reserved							
Changed BOOT to BTST in Figure 38							
Changed BOOT to BTST in Figure 39							
Changed bq24193 to bq24	192 in Figure 42						39

The datasheet number will be changing	The	datasheet	number	will be	changing
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Device Family	Change From:	Change To:
BQ24190/92/92I	SLUSAW5A	SLUSAW5B

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/sitesearch/docs/universalsearch.tsp?searchTerm=BQ24190#linkId=1&src=top

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

Electrical specification performance changes as indicated above.

Changes to product identification resulting from this PCN:

None.

Product Affected:

BQ24190RGER	BQ24192IRGER	BQ24192RGER	
BQ24190RGET	BQ24192IRGET	BQ24192RGET	

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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